

# Notice of Allowability

Application No.

10/619,157

Examiner

Steven D. Radosevich

Applicant(s)

THALMANN ET AL.

Art Unit

2117

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 7/14/03.
2. ☒ The allowed claim(s) is/are 1-18.
3. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a) ☒ All b) ☐ Some\* c) ☐ None of the:
    1. ☒ Certified copies of the priority documents have been received.
    2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
    3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.
  - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached
    - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_\_.
  - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

## Attachment(s)

1. ☒ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☒ Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date 7/14/03
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application
6. ☒ Interview Summary (PTO-413), Paper No./Mail Date \_\_\_\_\_.
7. ☒ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other \_\_\_\_\_.

GUY LAMARRE  
PRIMARY EXAMINER

### **DETAILED ACTION**

Claims 1-18 are present within this examination.

#### ***Priority***

Acknowledgment is made that foreign priority is claims for this application and as such the date 7/12/2002 is being used for this examination. Priority acknowledged to German application 102 31 680.5.

#### ***Information Disclosure Statement***

Acknowledgment is made that an Information Disclosure Statement (IDS) was filed by the applicant prior to this examination and as such has been fully reviewed and considered with respect to the claimed invention by the applicant. Examiner notes a single reference was disclosed within the provided IDS.

#### ***Drawings***

Acknowledgment is made that the figures as they have been submitted to the office do not at this time appear to have any issues that would require an objection and/or correction at this time. Therefore the office accepts the figures at this time.

### **EXAMINER'S AMENDMENT**

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided

Art Unit: 2117

by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Werner Stemer on 6/21/2007.

The application has been amended as follows:

With respect to the claims, the preamble of claims 11, 12, 17, and 18 have been amended to properly identify them as a "method for testing an integrated memory" as in the independent claim 10 to read as follows:

11. In line 1 after "the" insert "method for testing an."
12. In line 1 after "the" insert "method for testing an."
17. In line 1 after "the" insert "method for testing an."
18. In line 1 after "the" insert "method for testing an.":

With respect to the claim, claims 13-16 have been amended in the preamble and to drop the second "of the" within the claim where it reads "of the of the" to read as follows:

13. The method for testing an integrated memory according to claim 12, which further comprises connecting the signal terminal to one of the inputs of the register decoder circuit and to one of the inputs of the command decoder in a manner that allows changeover by a test mode signal.

14. The method for testing an integrated memory according to claim 13, which further comprises connecting the signal terminal to one of

the inputs of the register decoder circuit and to one of the inputs of the command decoder in a manner that allows changeover by a test mode signal.

15. The method for testing an integrated memory according to claim 12, which further comprises changing over a connection of the signal terminal from one of the register decoder circuit to one of the inputs of the command decoder dependent upon a test mode signal.

16. The method for testing an integrated memory according to claim 13, which further comprises changing over a connection of the signal terminal from one of the inputs of the register decoder circuit to one of the inputs of the command decoder dependent upon a test mode signal.

### ***Allowable Subject Matter***

The following is an examiner's statement of reasons for allowance:

Claims 1-18 are allowable.

The present invention pertains to testing a memory with a register decoder accessing registers containing test patterns used within a test operation. The claimed invention recites features such as: "command terminals for receiving command signals in a normal operation of the memory and in a test operation of the memory; a signal terminal for receiving a further signal different from the command signals; registers storing at least one of data patterns and data topologies used in the test operation of the memory; and a register decoder circuit connected to said registers for selecting said

Art Unit: 2117

registers, said register decoder circuit having inputs connected to said command terminals and to said signal terminal for selecting said registers in the test operation, said register decoder circuit selecting said registers in the test operation for reading out the at least one of data patterns and data topologies from said registers."

None of the prior art, either taken by itself or in any combination with any other prior art(s), would have anticipated or made obvious the following combination of limitations within the above limitations at or before the time the invention within this application was filed: "a register decoder circuit connected to said registers for selecting said register, said register decoder circuit having inputs connected to said command terminals and to said signal terminal for selecting said registers in the test operation, said register decoder circuit selecting said registers in the test operation for reading out the at least one of data patterns and data topologies from said registers."

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

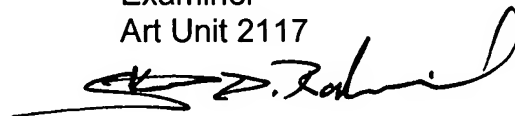
### ***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Steven D. Radosevich whose telephone number is 571-272-2745. The examiner can normally be reached on 9am-5:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jacques H. Louis can be reached on 571-272-6962. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Steven D. Radosevich  
Examiner  
Art Unit 2117



GUY LAMARRE  
PRIMARY EXAMINER